

**ABSTRACT OF THE DISCLOSURE**

For an LSI generated on a single semiconductor substrate, a built-in CPU core, a memory controller, an external bus interface that can connect an

5 external CPU chip from outside of the single semiconductor substrate, and a system bus bridge that mutually connects the built-in CPU core, the memory controller and the external bus interface are provided, so that the system structure can flexibly

10 be changed by adding or exchanging an external CPU chip according to required performance.